Session C19

Oversampling Data Converters

Thursday, June 21, 10:05 a.m.
TAPA 1

Co-Chairs: E. Martens, imec
M. Fukazawa, Renesas Electronics Corp.

C19-1 - 10:05 a.m.
A 1.2V 68µW 98.2dB-DR Audio Continuous-Time Delta-Sigma Modulator, C. Lee, M. Jang, Y. Chae, Yonsei University

C19-2 - 10:30 a.m.
A 0.029mm² 17-fJ/Conv.-Step CT ΔΣ ADC With 2nd-Order Noise-Shaping SAR Quantizer, J. Liu, S. Li*, W.Guo*, G. Wen, N. Sun*, University of Electronic Science and Technology of China, *The University of Texas at Austin

C19-3 – 10:55 a.m.
A 77dB SNDR 12.5MHz Bandwidth 0-1 MASH ΣΔ ADC Based on the Pipelined-SAR Structure, Y. Song, Y. Zhu, C.-H. Chan, L. Geng*, R. P. Martins, University of Macau, *Xi’an Jiaotong University

C19-4 - 11:20 a.m.
A 1.25MS/s Two-Step Incremental ADC with 100dB DR and 110dB SFDR, T. Katayama, S. Miyashita, K. Sobue, K. Hamashita, Asahi Kasei Microdevices

C19-5 – 11:45 a.m.